Sold c 98

Class | Subclass | ISSUE CLASSIFICATION

PATENT NUMBER

U.S. UTILITY Pat nt Application

O.I.P.E.	PATENT DATE
SCANNED BKD O.A. LP	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/773522	D	~~257~~ 427	58	~~2811~~ 1762	Talbot

APPLICANTS: Paul Farrar
Jerome Eldridge

TITLE: Micro C-4 semiconductor die and method for depositing connection sites thereon

PTO-2040 12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)	
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)

INTERNATIONAL CLASSIFICATION

☒ Continued on Issue Slip Inside File Jacket

☐ TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
☐ The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
	(Assistant Examiner)		(Date)		
☐ The term of this patent shall not extend beyond the expiration date of U.S Patent, No. _____				ISSUE FEE	
	(Primary Examiner)		(Date)	Amount Due	Date Paid
☐ The terminal ____months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
	(Legal Instruments Examiner)		(Date)		

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